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Luca Benini, Giovanni de Micheli

ACM Transactions on Design Automation of Electronic Systems (TODAES) April 2000 Volume 5 Issue 2

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic sytems consisting of a hardware platform and software layers. We consider the three major constituents of hardware that consume energy, namely computation, communication, and storage units, and we review methods of reducing their energy consumption. We also study models for analyzing the energy cost of software, and methods for energy-efficient software design and compilation. This survery ...

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